Fig. 1

BLOCK DIAGRAM ILLUSTRATING THE FIRST EMBODIMENT

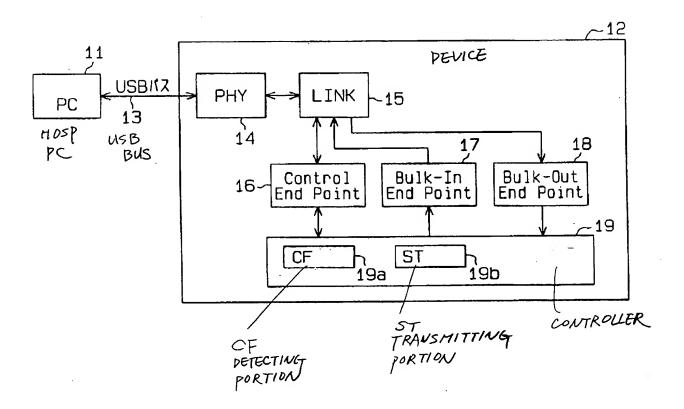


Fig. 2

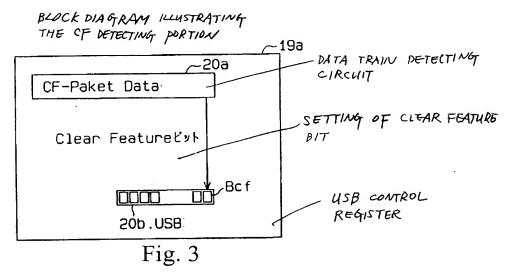


DIAGRAM FOR DESCRIBING OPERATIONS IN OCCURRENCE OF ERRONEOUS RECOGNITION

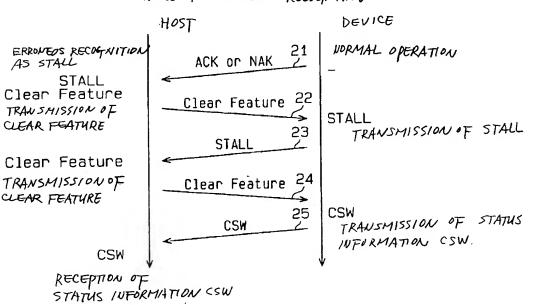


Fig. 4

BLOCK DIAGRAM ILLUSTRATING THE SECOND EMBODIMENT

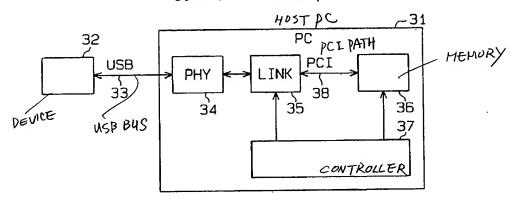


Fig. 5

DIAGRAM FOR DESCRIBING OPERATIONS IN OCCURRENCE OF GREAR OF DEVICE DEVICE HOST RECOGNITION OF STALL STALL 41 STALL GENERATION OF ERROR DATA 42 DATA TRANSMISSION く STALL OF DATA TRANSMISSION OF STALL 43 RECOGNITION OF STA STALL STALL Clear Feature Clear Feature TRANSMISSION OF LLEAR FEATURE 45 CSW TRANSMISSION OF STATUS CSW INFORMATION CSW CSW RECEPTION OF

STATUS INFORMATION CSW

Fig. 6

DIAGRAM FOR DESCRIBING OPERATIONS IN OCCURRENCE OF GRRONEOS RECOGNITION

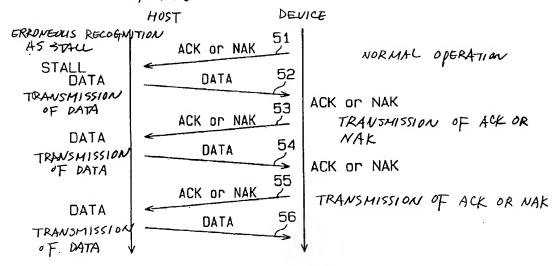


Fig. 7

BLOCK DIAGRAM ILLUSTRATING THE THIRD EMBODIMENT

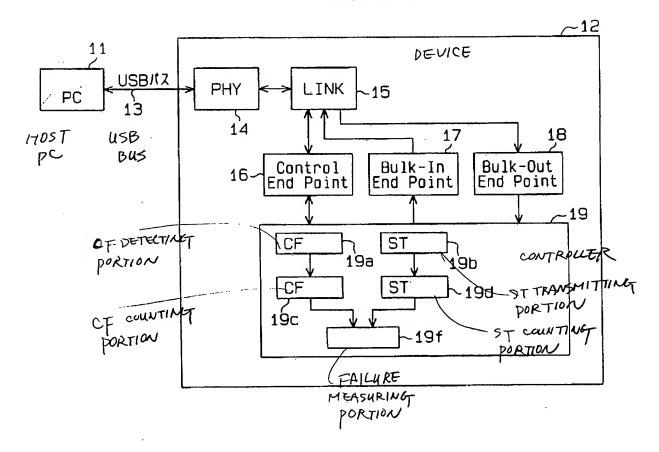


Fig. 8

DIAGRAM ILLUSTRATINGS FORMATS OF PACKETS

	<u>-</u> -		ğι		EOP ~P3	2bit		
	EOP ~P1	2bit	EOP ~ P2	2bit	CRC16	16bit		
	CRC5	Sbit	CRC5	4bit 5bit				
	Frame Number	11bit	ENDP	ł .	DATA		Р4	
			ADDR	7bit			E0P ~ P4	2bit
	PID	8bit	PID	8bit	PIO	8bit	PID	8bit
	SYNC	8bit	SYNC	8bit	SYNC	8bit	SYNC	8bit
יי	SOF PACKET SYNC	-	TO KEN PACKET		DATA DACKET		HANDSHAKE	

Fig. 9

DIAGRAM FOR DESCRIBING OPERATIONS IN OCCURRENCE OF ERROR OF DEVICE

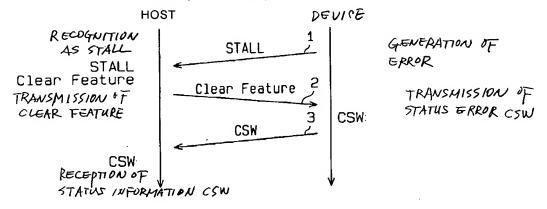


Fig. 10

DIAGRAM ILLUSTRATING
OPERATIONS IN OCCURRENCE OF
ERRONEOS RECOGNITION

